CMPS 5203

Tentative agenda:

Aug 23- Embedded systems overview
Aug 25- Customized processors
Aug 30- Sequential components

Sep 1- Single-purpose processor basic model
Sep 6- Labor Day
Sep 8- Register transfer design
Sep 13- General-purpose processor software - Assembly-level instructions
Sep 15- Microprocessor architecture
         Assignments # 1/2
Sep 20- Assembly language
Sep 22- Parallel port driver
Sep 27- Application specific instruction set processors
Sep 29- Peripherals
         Assignment # 3

Oct 4- Controllers
Oct 6- Memory
         Assignment # 4
Oct 11- Memory hierarchy
Oct 13- Test # 1
Oct 18- Advanced RAM
Oct 20- Interfacing
Oct 25- I/O addressing
Oct 27- Interrupt address table
         Assignment # 5

Nov 1- Arbitration
Nov 3- Serial protocols
         Assignment # 6
Nov 8- Digital camera
Nov 10- Design implementations
Nov 15- Concurrent processes
Nov 17- Dataflow model
Nov 22- Test # 2
Nov 24- Thanksgiving Holidays
Nov 29- Multiple processes implementation

Dec 1- Control systems

Dec 6- Finals (Monday, 8:00 pm)