Tentative agenda:

Aug 24 - Introduction
Aug 25 - LAB 1 - CPU registers, DOS operating system
Aug 26 - Data representation
Aug 31 - Data representation

Sep 1 - LAB 2 - Binary numbers
Sep 2 - Computer evolution
Sep 7 - Computer evolution
Sep 8 - LAB 3 - Binary numbers, ASCII code
Sep 9 - Computer evolution
    Assignment # 1
Sep 14 - Computer performance
Sep 15 - LAB 4 - Addressing memory, simple arithmetic
Sep 16 - Computer components
    Assignment # 2
Sep 21 - Interrupts
Sep 22 - LAB 5 - Arithmetic operations, conditional jump, flags
Sep 23 - Bus systems
    Assignment # 3
Sep 28 - Bus systems - Arbitration
Sep 29 - LAB 6 - Conditional jumps, loops
Sep 30 - Cache memory - basics
    Assignment # 4

Oct 5 - Cache memory mapping
Oct 6 - LAB 7 - review
Oct 7 - Test # 1
Oct 12 - Cache memory mapping
Oct 13 - LAB 7a - Boolean operations
Oct 14 - Cache memory replacement algorithms
Oct 19 - Memory
Oct 20 - LAB 8 - Bit manipulation
Oct 21 - Error correction
Oct 26 - Error correction
Oct 27 - LAB 9 - Arrays and strings
Oct 28 - Memory technology

Nov 2 - Memory technology
Nov 3 - LAB 10 - Logical instructions
Nov 4 - External memory - disks - RAID - Optical memory
    Assignment # 5
Nov 9 - Input/output
Nov 10- LAB 11 - Procedures
Nov 11- Input/output
           Assignment # 6
Nov 16- DMA
Nov 17- LAB 12 - Procedures and stacks
Nov 18- Test # 2
Nov 23- Virtual memory
Nov 24- Thanksgiving Break
Nov 25- Thanksgiving Break
Nov 30- Virtual memory

Dec 1- LAB 13 - Programming practice
Dec 2- Segmentation

Dec 9- Finals (Thursday, 1:00 pm)